



ACE11H1PB

Dual P-Channel Enhancement Mode Field Effect Transistor

Description

The ACE11H1PB uses advanced trench technology to provide excellent $R_{DS(ON)}$ and low gate charge. This device is suitable for use as a load switch or in PWM applications.

Features

- $V_{DS} (V) = -20V$
- $I_D (V_{GS} = -4.5V) = -3A$
- $R_{DS(ON)} < 85m\Omega @ V_{GS} = -4.5V$
- $R_{DS(ON)} < 115m\Omega @ V_{GS} = -2.5V$
- $R_{DS(ON)} < 150m\Omega @ V_{GS} = -1.8V$
- SOT23-6 Package

Absolute Maximum Ratings

| Parameter | | Symbol | Max | Unit |
|---|--------------------|----------------|------------|------------|
| Drain-Source Voltage | | V_{DSS} | 20 | V |
| Gate-Source Voltage | | V_{GSS} | ± 12 | V |
| Drain Current (Continuous) | $T_A = 25^\circ C$ | I_D | -3 | A |
| | $T_A = 70^\circ C$ | | -2.4 | |
| Drain Current (Pulse) ^C | | I_{DM} | -13 | |
| Power Dissipation ^B | $T_A = 25^\circ C$ | P_D | 1.15 | W |
| | $T_A = 70^\circ C$ | | 0.73 | |
| Operating and Storage Temperature Range | | T_J, T_{STG} | -55 to 150 | $^\circ C$ |

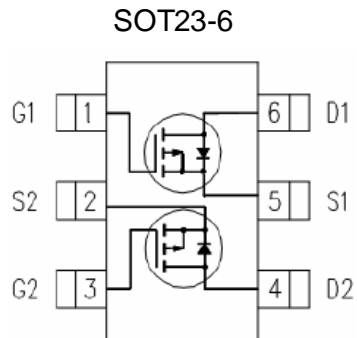
- A. The value of $R_{\theta JA}$ is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with $T_A = 25^\circ C$. The value in any given application depends on the user's specific board design.
- B. The power dissipation P_D is based on $T_{J(MAX)} = 150^\circ C$, using $\leq 10s$ junction-to-ambient thermal resistance.
- C. Repetitive rating, pulse width limited by junction temperature $T_{J(MAX)} = 150^\circ C$. Ratings are based on low frequency and duty cycles to keep initial $T_J = 25^\circ C$.
- D. The $R_{\theta JA}$ is the sum of the thermal impedance from junction to lead $R_{\theta JL}$ and lead to ambient.
- E. The static characteristics in Figures 1 to 6 are obtained using $< 300\mu s$ pulses, duty cycle 0.5% max.
- F. These curves are based on the junction-to-ambient thermal impedance which is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, assuming a maximum junction temperature of $T_{J(MAX)} = 150^\circ C$. The SOA curve provides a single pulse rating.



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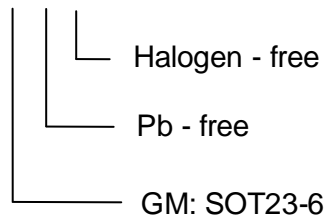
Dual P-Channel Enhancement Mode Field Effect Transistor

Packaging Type



Ordering information

ACE11H1PB XX + H





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Electrical Characteristics $T_A=25\text{ }^\circ\text{C}$ unless otherwise noted

| Parameter | Symbol | Conditions | Min. | Typ. | Max. | Unit |
|---------------------------------------|---------------|--|------|-------|-----------|------------|
| Static | | | | | | |
| Drain-Source Breakdown Voltage | $V_{(BR)DSS}$ | $V_{GS} = 0V, I_D = -250\mu A$ | -20 | | | V |
| Zero Gate Voltage Drain Current | I_{DSS} | $V_{DS} = -20V, V_{GS} = 0V$ | | | -1 | μA |
| Gate Threshold Voltage | $V_{GS(th)}$ | $V_{GS} = V_{DS}, I_{DS} = -250\mu A$ | -0.5 | -0.6 | -1 | V |
| Gate Leakage Current | I_{GSS} | $V_{GS} = \pm 12V, V_{DS} = 0V$ | | | ± 100 | nA |
| Static Drain-Source On-Resistance | $R_{DS(ON)}$ | $V_{GS} = -4.5V, I_D = -2.8A$ | | 80 | 85 | m Ω |
| | | $V_{GS} = -2.5V, I_D = -2A$ | | 95 | 115 | |
| | | $V_{GS} = -1.8V, I_D = -2A$ | | 119 | 150 | |
| Forward Transconductance | g_{FS} | $V_{DS} = -5V, I_D = -2.5A$ | | 13 | | S |
| Diode Forward Voltage | V_{SD} | $I_{SD} = -1.6A, V_{GS} = 0V$ | | -0.81 | -1.0 | V |
| Maximum Body-Diode Continuous Current | I_S | | | | -1.6 | A |
| Switching | | | | | | |
| Total Gate Charge | Q_g | $V_{DS} = -6V, I_D = -2.8A$ $V_{GS} = -4.5V$ | | 6.6 | 8.6 | nC |
| Gate-Source Charge | Q_{gs} | | | 0.3 | 0.4 | |
| Gate-Drain Charge | Q_{gd} | | | 1.3 | 1.7 | |
| Turn-On Delay Time | $T_{d(on)}$ | $V_{DD} = -6V, R_L = 6\Omega$ $I_D = -1A, V_{GEN} = -4.5V$ $R_G = 6\Omega$ | | 9.7 | 19.4 | ns |
| Turn-On Rise Time | t_r | | | 3.6 | 7.1 | |
| Turn-Off Delay Time | $t_{d(off)}$ | | | 33.3 | 66.6 | |
| Turn-Off Fall Time | t_f | | | 4.5 | 9 | |
| Dynamic | | | | | | |
| Input Capacitance | C_{iss} | $V_{GS} = 0V, V_{DS} = -6V$ $f = 1.0MHz$ | | 589 | | pF |
| Output Capacitance | C_{oss} | | | 91.2 | | |
| Reverse Transfer Capacitance | C_{rss} | | | 67.2 | | |



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Typical Performance Characteristics

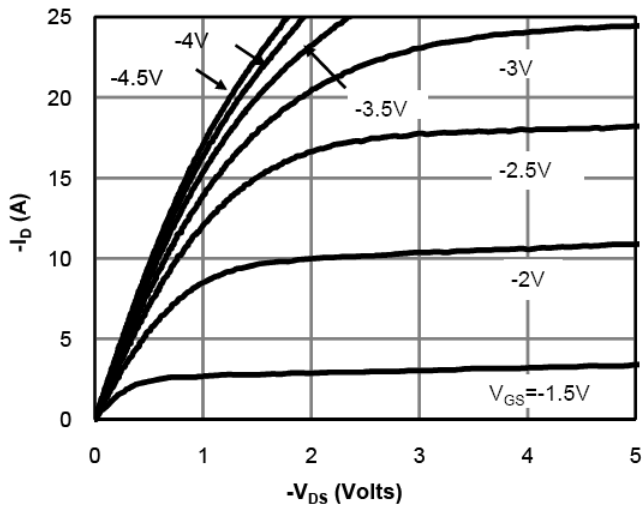


Figure 1: On-Region Characteristics

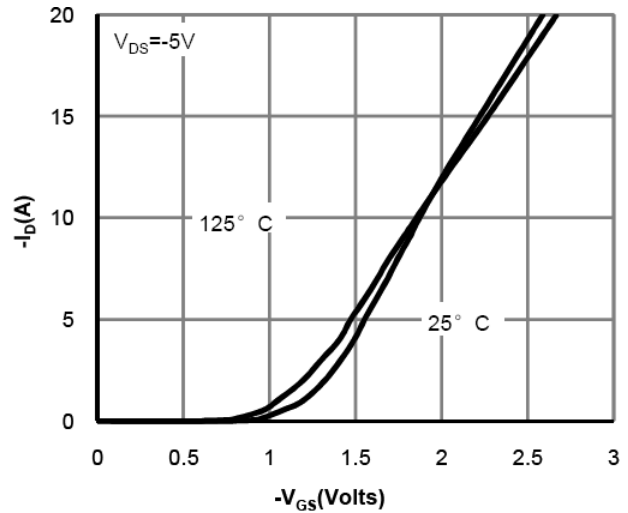


Figure 2: Transfer Characteristics

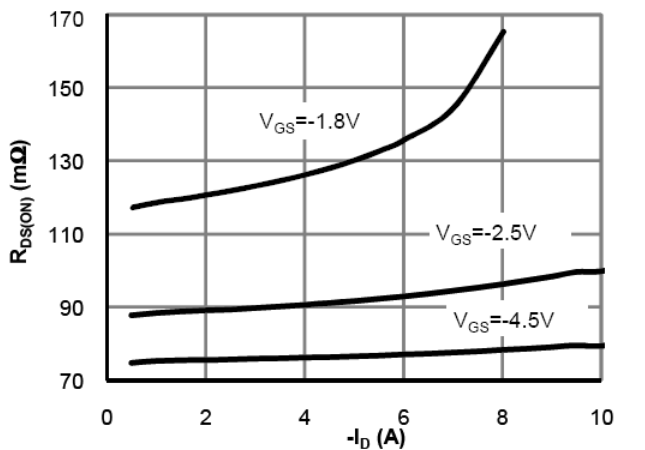


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

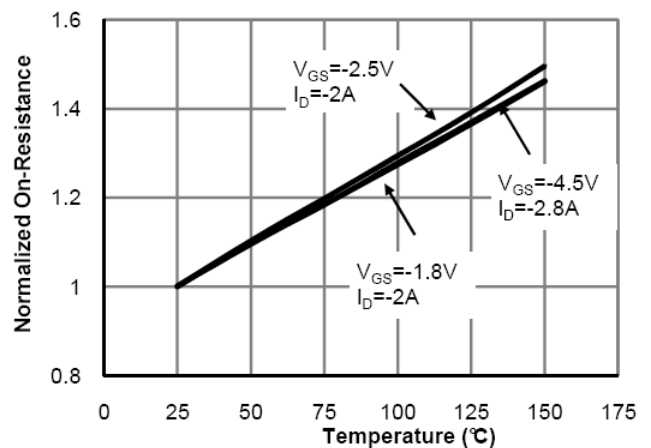


Figure 4: On-Resistance vs. Junction Temperature

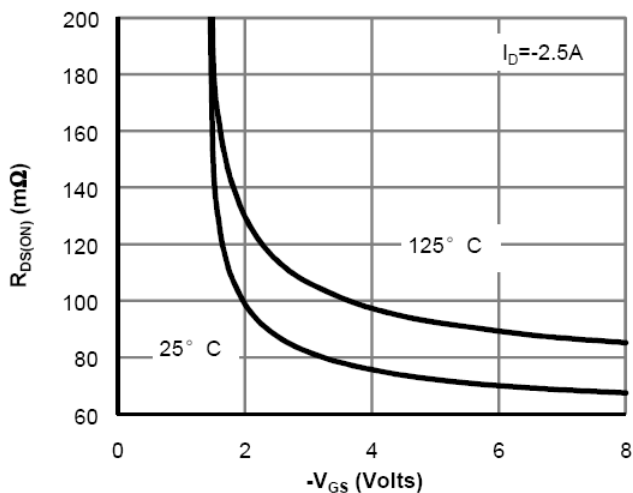


Figure 5: On-Resistance vs. Gate-Source Voltage

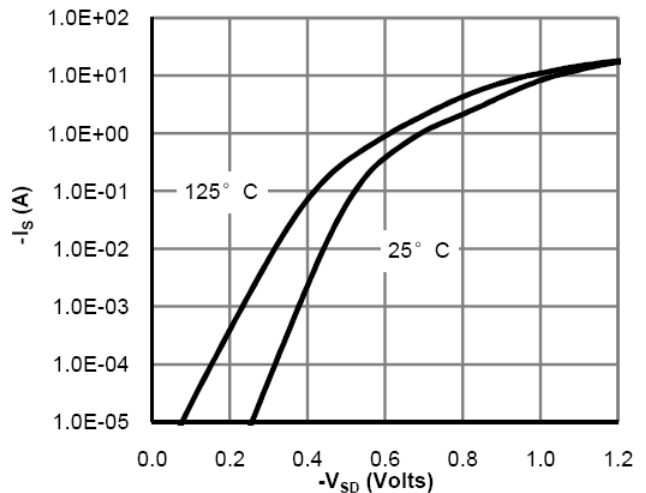


Figure 6: Body-Diode Characteristics



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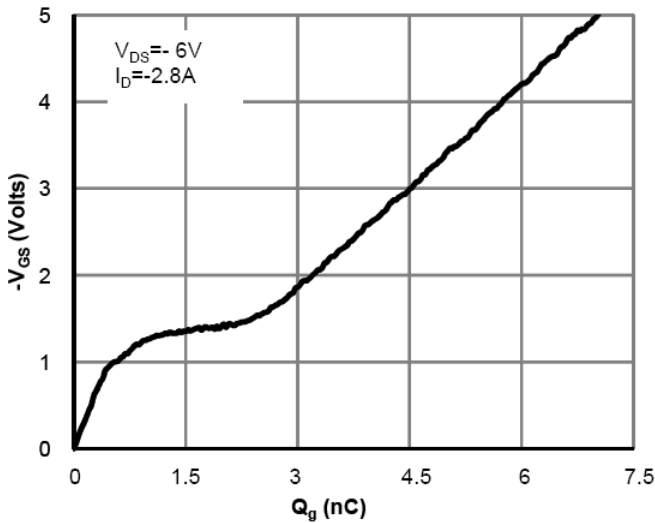


Figure 7: Gate-Charge Characteristics

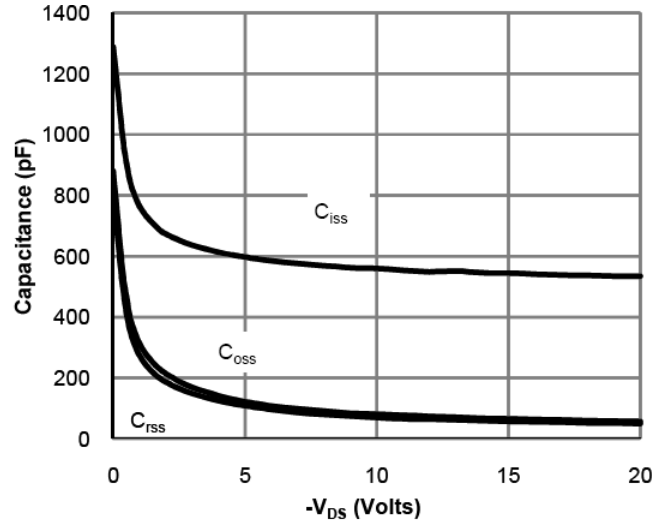


Figure 8: Capacitance Characteristics

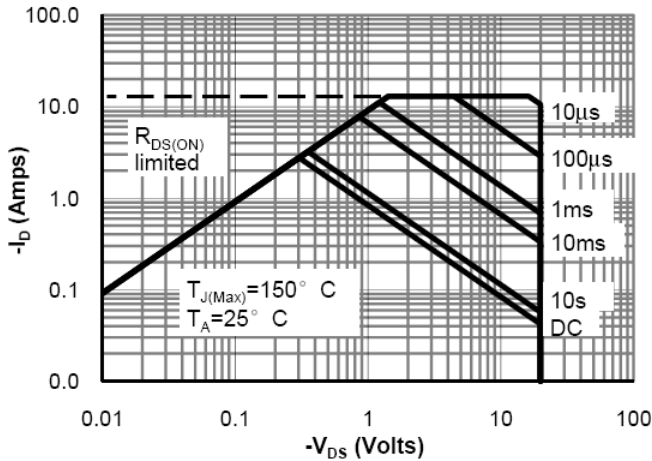


Figure 9: Maximum Forward Biased Safe Operating Area

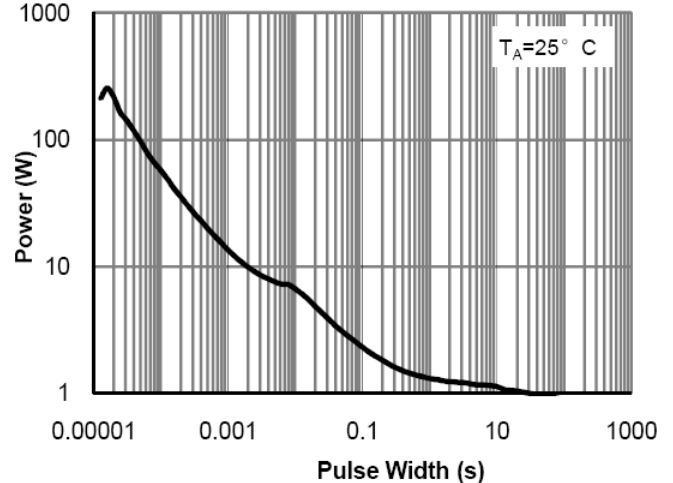


Figure 10: Single Pulse Power Rating Junction-to-Ambient

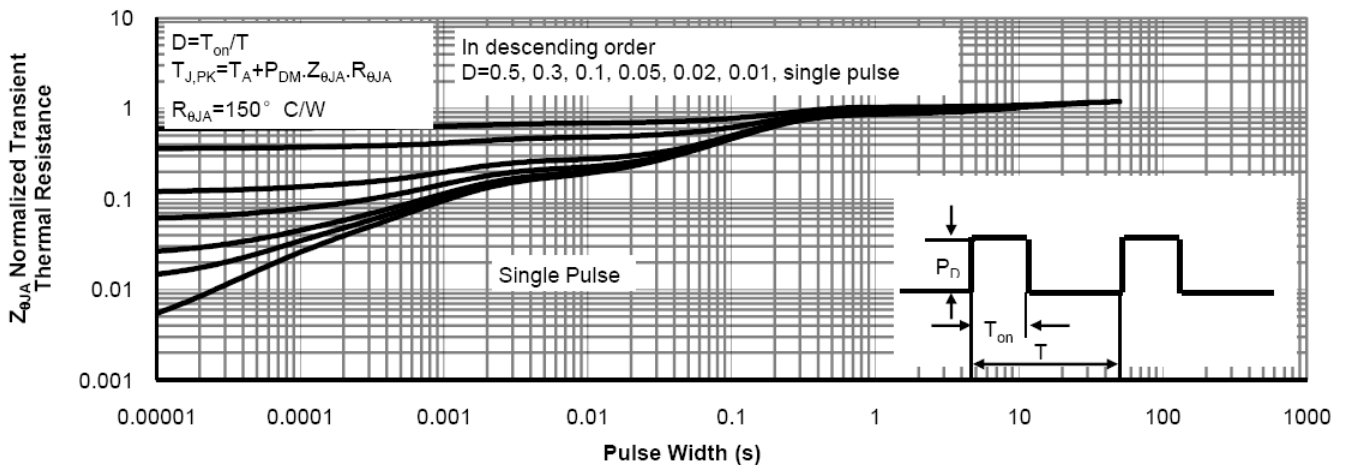


Figure 11: Normalized Maximum Transient Thermal Impedance

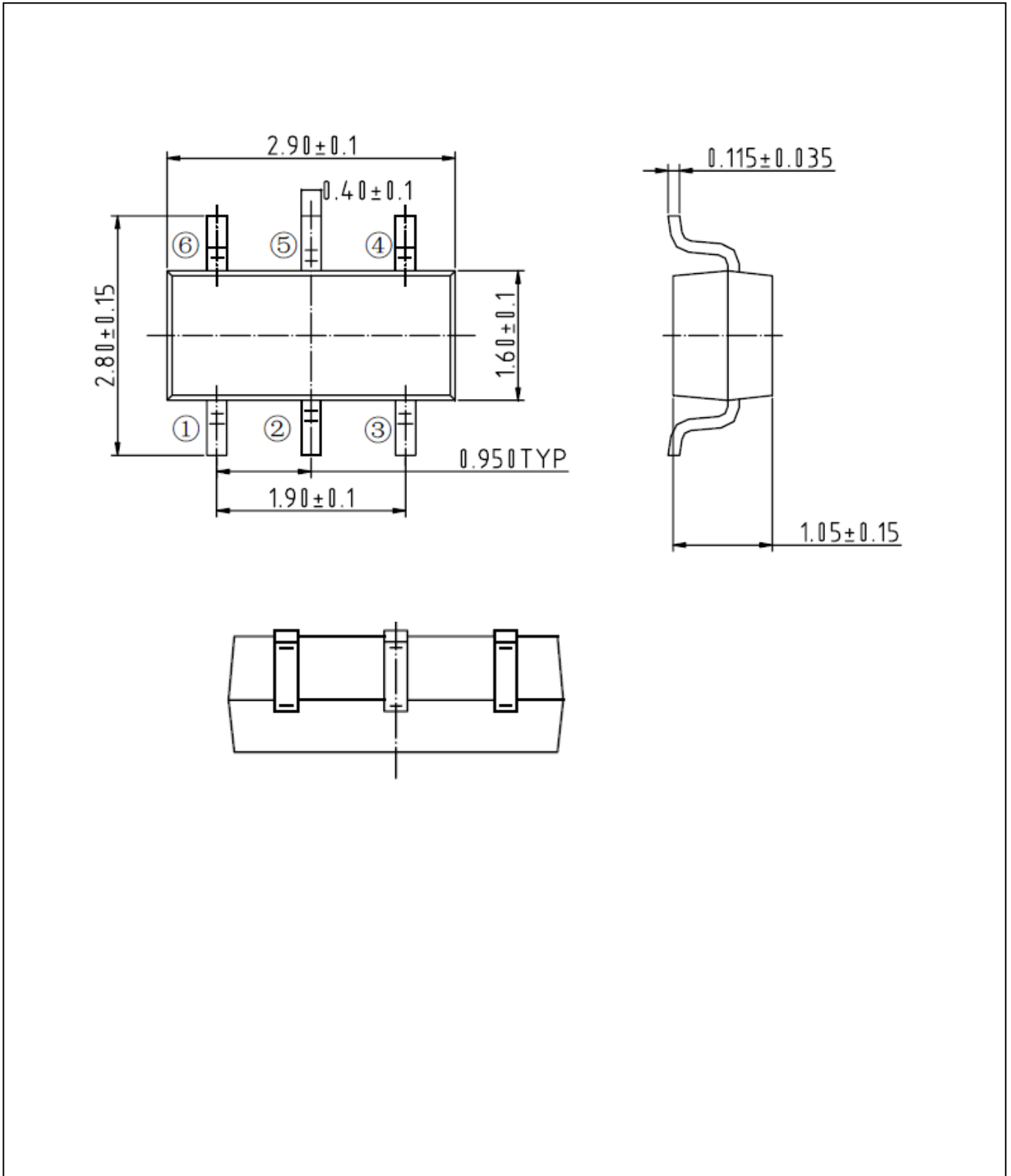


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Packing Information

SOT23-6





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Notes

ACE does not assume any responsibility for use as critical components in life support devices or systems without the express written approval of the president and general counsel of ACE Electronics Co., LTD. As sued herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury to the user.
2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

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