



ACE1358B

N-Channel Enhancement Mode Field Effect Transistor

Description

The ACE1358B uses advanced trench technology to provide excellent R_{DS} , low gate charge and operation with gate voltages as low as 2.5V.

-RoHS Compliant

-Halogen Free

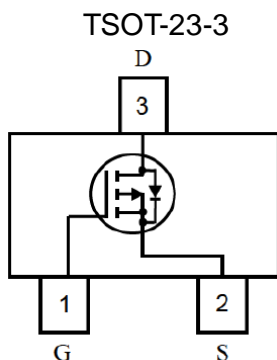
Features

- $V_{DS} (V) = 20V, I_D = 2A$
- $R_{DS(ON)} < 50m\Omega @ V_{GS} = 4.5V$
- $R_{DS(ON)} < 80m\Omega @ V_{GS} = 2.5V$
- TSOT-23-3 Package

Absolute Maximum Ratings

Parameter	Symbol	Max	Unit
Drain-Source Voltage	V_{DSS}	20	V
Gate-Source Voltage	V_{GSS}	± 12	V
Drain Current (Continuous)	I_D	$T_A = 25^\circ C$	2
		$T_A = 70^\circ C$	1.5
Drain Current (Pulsed)	I_{DM}	8	A
Power Dissipation	$T_A = 25^\circ C$	P_D	0.8
Operating temperature / storage temperature	T_J / T_{STG}	-55~150	$^\circ C$

Packaging Type



Ordering information

ACE1358B XXX + H

- └─ Halogen - free
- └─ Pb - free
- └─ BMS: TSOT-23-3



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Electrical Characteristics

$T_A=25^{\circ}\text{C}$, unless otherwise specified.

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Static						
Drain-source breakdown voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=250\mu A$	20			V
Zero gate voltage drain current	I_{DSS}	$V_{DS}=20V, V_{GS}=0V$			1	μA
Gate threshold voltage	$V_{GS(th)}$	$V_{GS}=V_{DS}, I_{DS}=250\mu A$	0.5	0.75	1.1	V
Gate leakage current	I_{GSS}	$V_{GS}=\pm 12V, V_{DS}=0V$			± 100	nA
Drain-source on-state resistance	$R_{DS(ON)}$	$V_{GS}=4.5V, I_D=2A$		36	50	m Ω
		$V_{GS}=2.5V, I_D=1A$		46	80	
Forward trans conductance	g_{FS}	$V_{DS}=5V, I_D=2A$		6		S
Diode forward voltage	V_{SD}	$I_{SD}=1A, V_{GS}=0V$		0.75	1.0	V
Switching						
Total gate charge	Q_g	$V_{GS}=4.5V, V_{DS}=10V, I_D=2A$		2.4		nC
Gate-source charge	Q_{gs}			0.3		
Gate-drain charge	Q_{gd}			0.8		
Turn-on delay time	$t_{d(on)}$	$V_{DD}=10V, R_L=3\Omega, I_D=1A,$ $V_{GEN}=4.5V, R_G=6\Omega$		2.1		ns
Turn-on rise time	T_r			3.6		
Turn-off delay time	$t_{d(off)}$			23.3		
Turn-off fall time	T_f			4.5		
Dynamic						
Input capacitance	C_{iss}	$V_{GS}=0V, V_{DS}=10V, f=1.0MHz$		240		pF
Output capacitance	C_{oss}			40		
Reverse transfer capacitance	C_{rss}			22		

Notes:

1. Pulse width limited by maximum junction temperature.
2. Pulse test: $PW \leq 300\mu s$, duty cycle $\leq 2\%$.
3. For design AID only, not subject to production testing.
4. Switching time is essentially independent of operating temperature.



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Typical Performance Characteristics

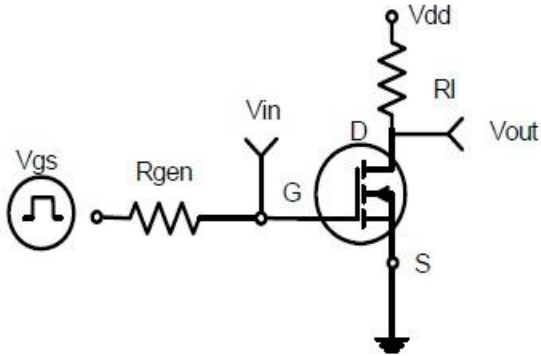


Figure 1: Switching Test Circuit

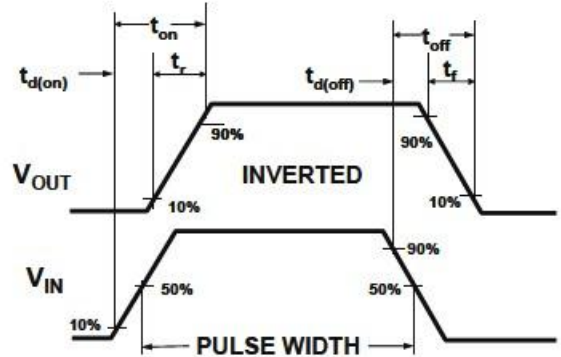


Figure 2: Switching Waveforms

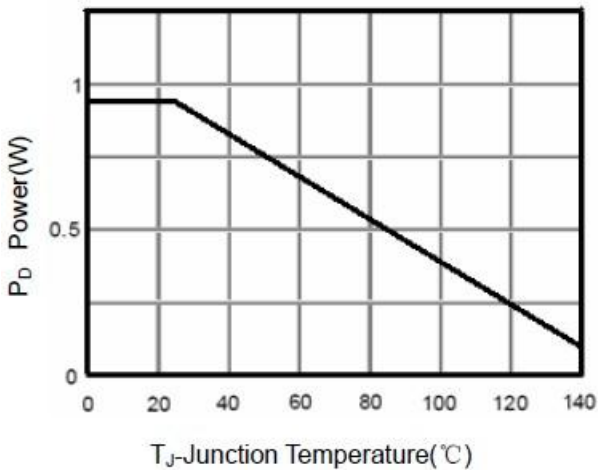


Figure 3 Power Dissipation

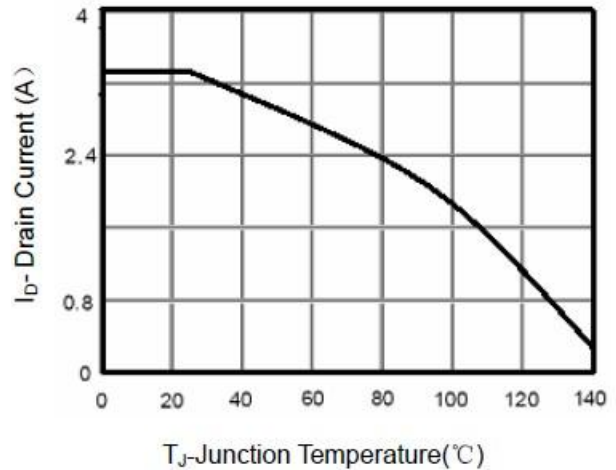


Figure 4 Drain Current

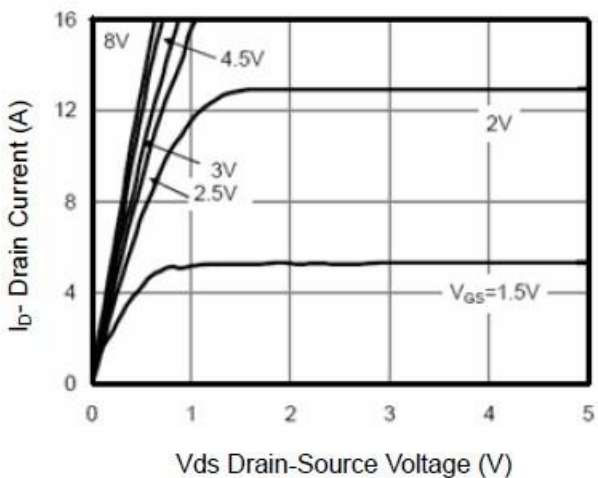


Figure 5 Output Characteristics

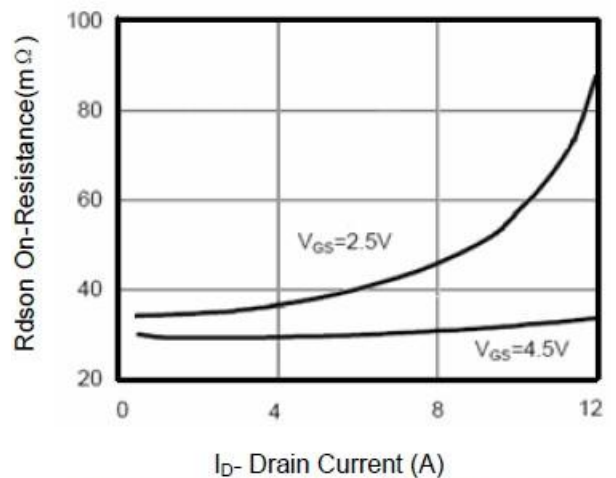


Figure 6 Drain-Source On-Resistance

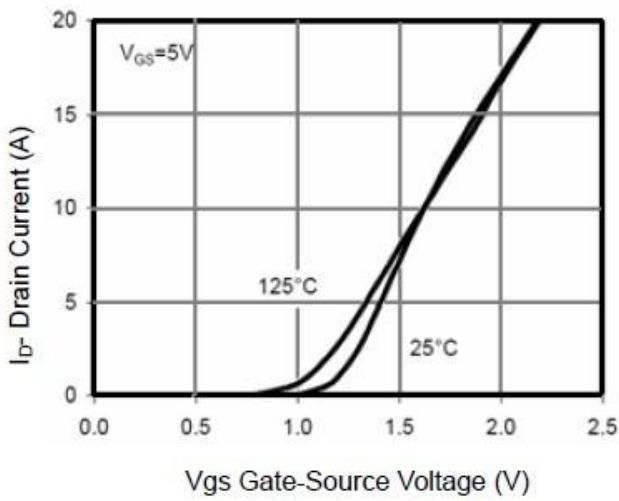


Figure 7 Transfer Characteristics

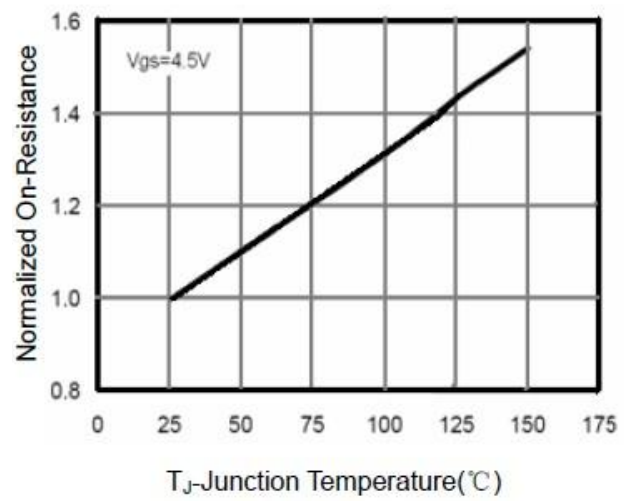


Figure 8 Drain-Source On-Resistance

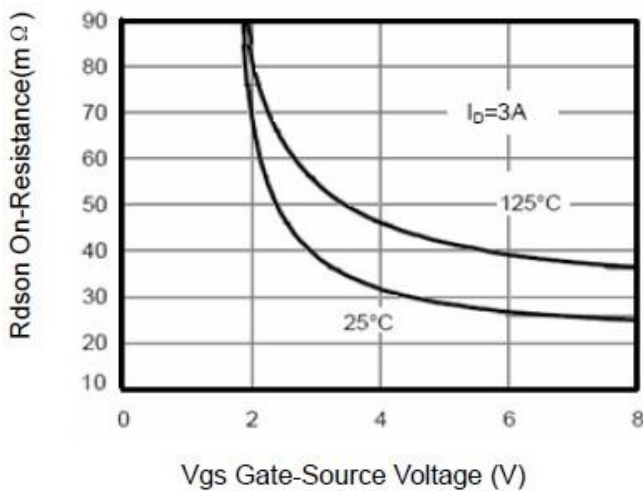


Figure 9 Rdson vs Vgs

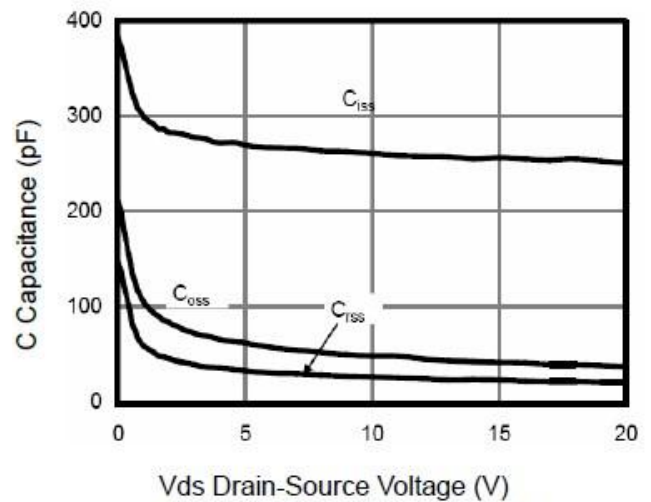


Figure 10 Capacitance vs Vds

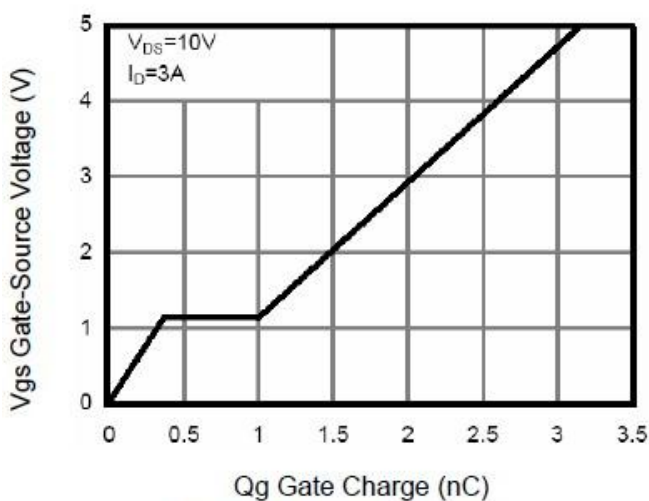


Figure 11 Gate Charge

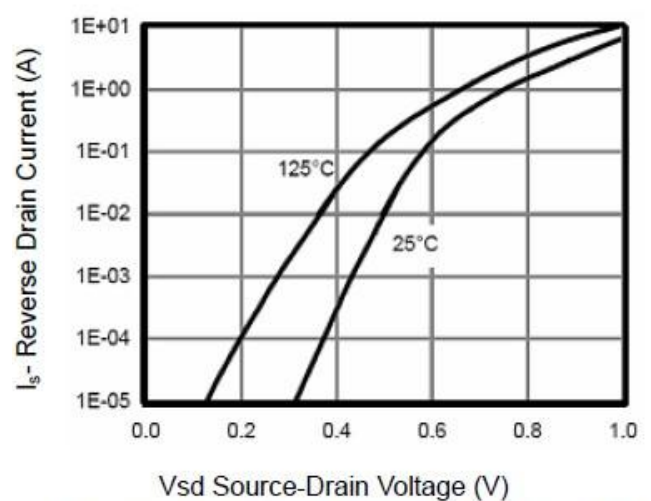


Figure 12 Source- Drain Diode Forward



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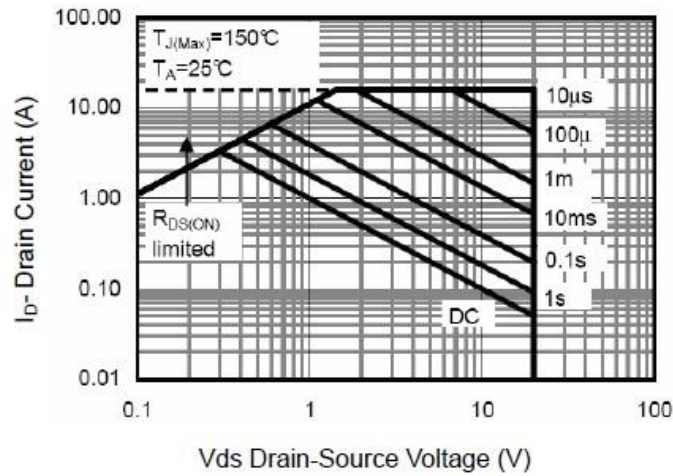


Figure 13 Safe Operation Area

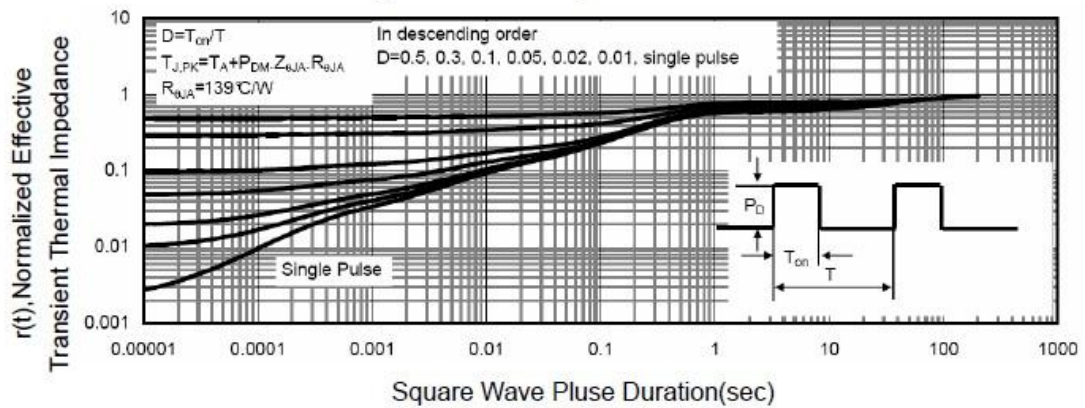


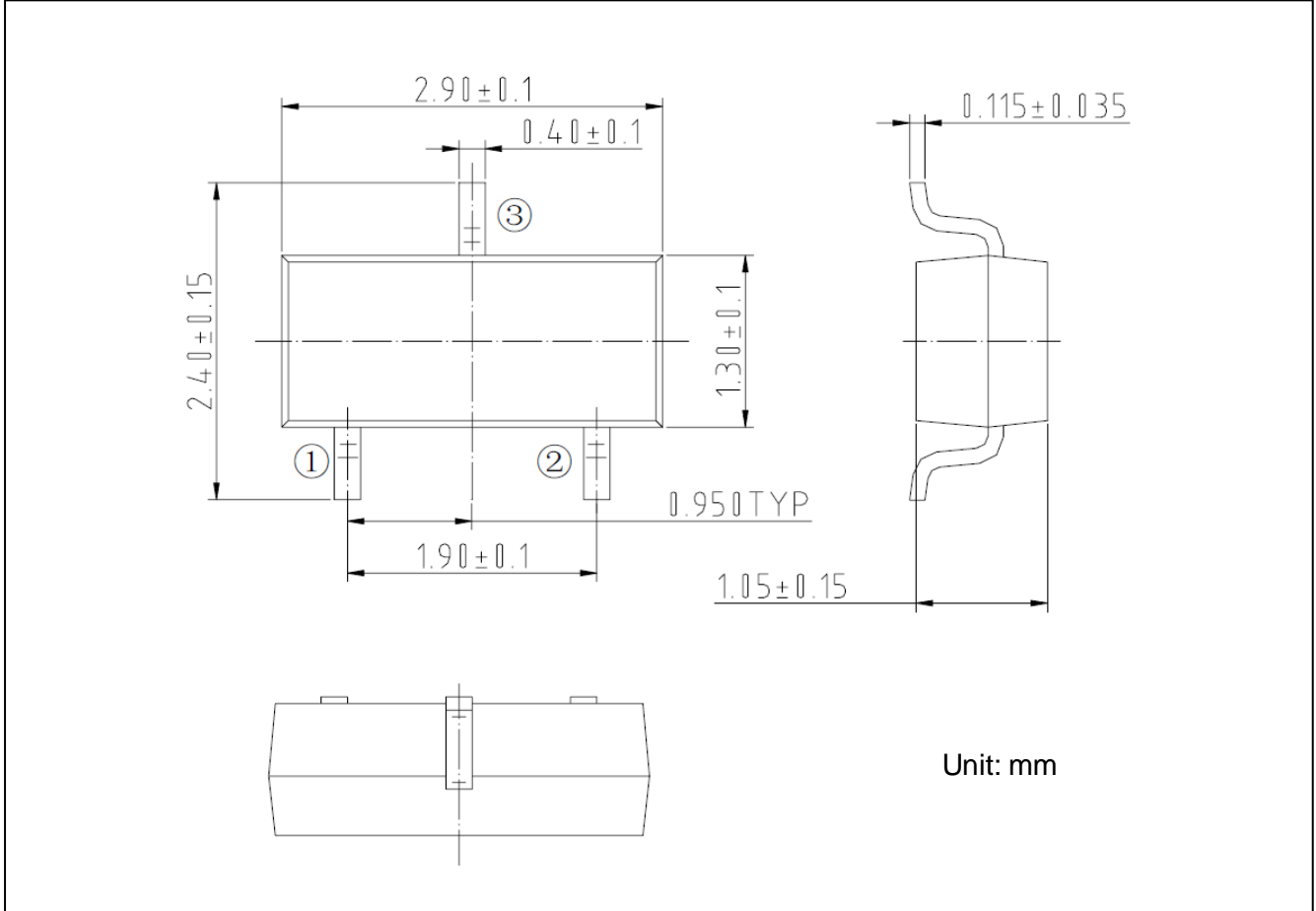
Figure 14 Normalized Maximum Transient Thermal Impedance



ACE1358B N-Channel Enhancement Mode Field Effect Transistor

Packing Information

TSOT-23-3





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Notes

ACE does not assume any responsibility for use as critical components in life support devices or systems without the express written approval of the president and general counsel of ACE Electronics Co., LTD. As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury to the user.
2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

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