

P-Channel Enhancement Mode Field Effect Transistor

Description

The ACE1561BE uses advanced trench technology to provide excellent $R_{\rm DS(ON)}$ and low gate charge. This device is suitable for use as a load switch or in PWM applications.

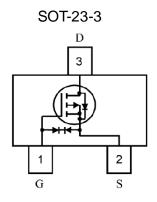
Features

- $V_{DS}(V) = -20V, I_{D} = -3A$
- $R_{DS(ON)} = 90 \text{m}\Omega @ V_{GS} = -4.5V$
- $R_{DS(ON)}$ =110m Ω @ V_{GS} = -2.5V
- SOT23-3 Package

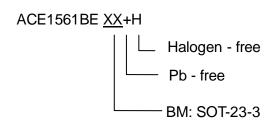
Absolute Maximum Ratings

Parameter		Symbol	Max	Unit
Drain-Source Voltage		V_{DSS}	-20	V
Gate-Source Voltage		V_{GSS}	±8	V
Drain Current (Continuous)	T _A =25°C		-3	Α
	T _A =70°C	· I _D	-2	A
Drain Current (Pulsed)		I _{DM}	-12	Α
Power Dissipation	T _A =25°C	P_{D}	1.3	W
Operating temperature / storage temperature		T _J /T _{STG}	-55~150	$^{\circ}\!\mathbb{C}$

Packaging Type



Ordering information





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Electrical Characteristics

T_A=25°C, unless otherwise specified.

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit			
Static									
Drain-source breakdown voltage	V _{(BR)DSS}	V_{GS} =0V, I_D =-250 μ A	-20			>			
Zero gate voltage drain current	I _{DSS}	V_{DS} =-20V, V_{GS} =0V			-1	μΑ			
Gate threshold voltage	$V_{GS(th)}$	$V_{GS}=V_{DS}$, $I_{DS}=-250\mu A$	-0.4		-1	V			
Gate leakage current	I _{GSS}	$V_{GS}=\pm 8V$, $V_{DS}=0V$			±10	nΑ			
Drain-source on-state resistance	R _{DS(ON)}	V_{GS} =-4.5V, I_D =-2.8A		90	110	mΩ			
		V_{GS} =-2.5V, I_D =-2A		110	150				
Forward trans conductance	g FS	V_{DS} =-5V, I_{D} =-2A		6.5		S			
Diode forward voltage	V _{SD}	I_{SD} =1A, V_{GS} =0V		-0.7	-1.3	V			
		Switching							
Total gate charge	Q_{g}			5.5		nC			
Gate-source charge	Q_gs	V_{GS} =4.5V, V_{DS} =10V, I_{D} =6A		1.5					
Gate-drain charge	Q_{gd}			1.3					
Turn-on delay time	t _{d(on)}	V_{DD} =-6V, R_L =6 Ω I_D =-1A, V_{GEN} =-4.5V R_G =6 Ω		8		ns			
Turn-on rise time	T _r			11					
Turn-off delay time	t _{d(off)}			33					
Turn-off fall time	t _f	11.6 032		15					
		Dynamic							
Input capacitance	C _{iss}	V _{GS} =0V, V _{DS} =-10V, f=1.0MHz		150		pF			
Output capacitance	C _{oss}			53					
Reverse transfer capacitance	C _{rss}	I—I.UIVII IZ		17					

Note:

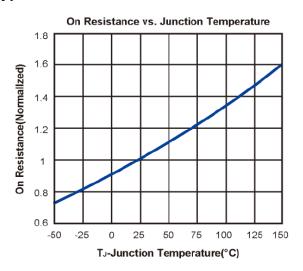
A: The value of $R_{\theta JA}$ is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with TA =25°C. The value in any given application depends on the user's specific board design. The current rating is based on the t≤ 10s thermal resistance rating.

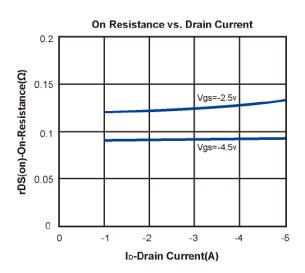
- B: Repetitive rating, pulse width limited by junction temperature.
- C: The $R_{\theta JA}$ is the sum of the thermal impedance from junction to lead $R_{\theta JL}$ and lead to ambient.

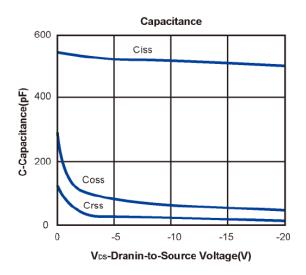


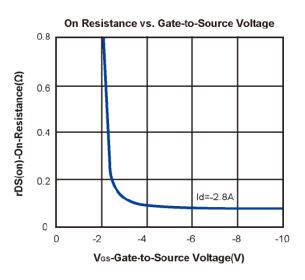
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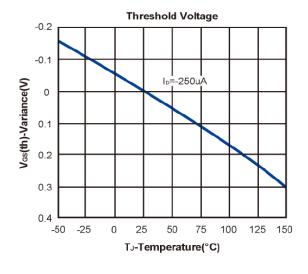
Typical Performance Characteristics

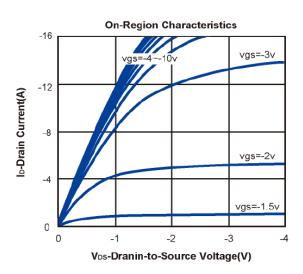






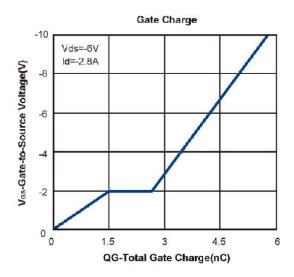


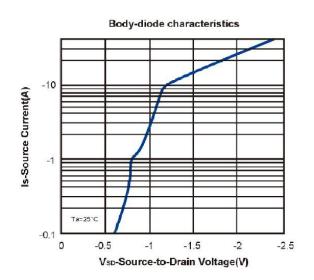






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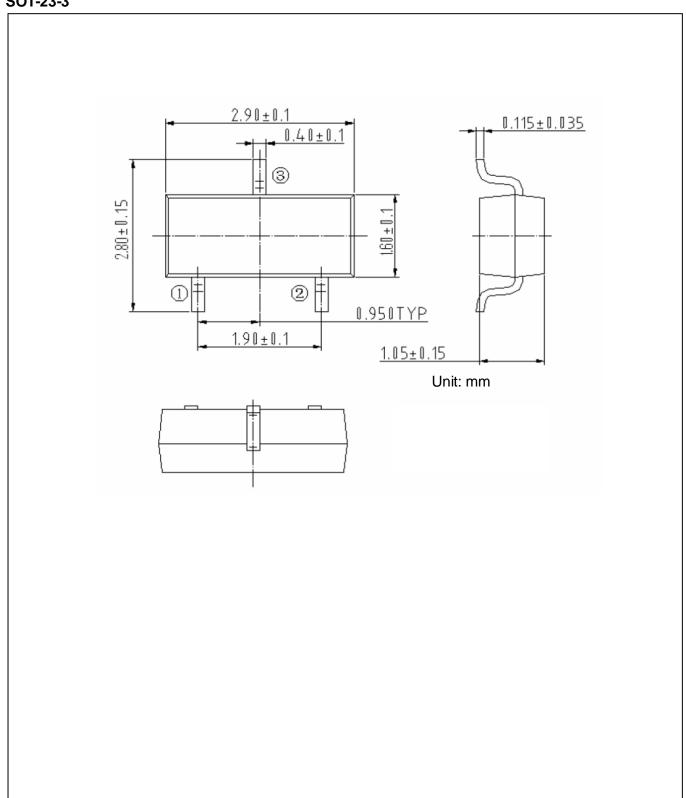




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Packing Information

SOT-23-3





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Notes

ACE does not assume any responsibility for use as critical components in life support devices or systems without the express written approval of the president and general counsel of ACE Electronics Co., LTD. As sued herein:

- 1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and shoes failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury to the user.
- 2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

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