



# ACE1710B

## N-Channel Enhancement Mode MOSFET

### Description

The ACE1710B uses advanced trench technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge. It can be used in a wide variety of applications

### Features

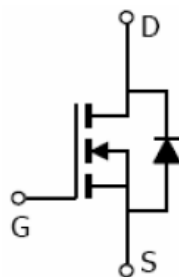
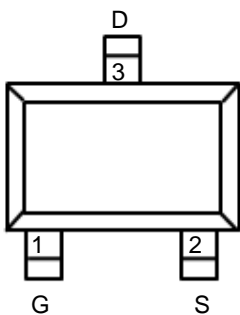
- 100V/1.5A
- $R_{DS(ON)} < 280m\Omega$  @  $V_{GS} = 10V$
- High density cell design for ultra low  $R_{DS(ON)}$
- Fully characterized avalanche voltage and current
- Excellent package for good heat dissipation
- SOT23 -3L Package

### Absolute Maximum Ratings $T_A=25^\circ C$ unless otherwise noted

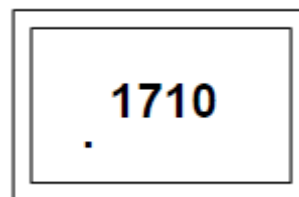
Parameter	Symbol	Ratings	Unit	
Drain-Source Voltage	$V_{DSS}$	100	V	
Gate-Source Voltage	$V_{GSS}$	$\pm 20$	V	
Drain Current (Continuous)	$I_D$	$T_A=25^\circ C$	1.5	A
		$T_A=70^\circ C$	1.2	
Drain Current (Pulsed)	$I_{DM}$	5	A	
Power Dissipation	$P_D$	1	W	
Operating temperature / storage temperature	$T_J/T_{STG}$	-55~150	$^\circ C$	

### Packaging Type

SOT-23-3 L

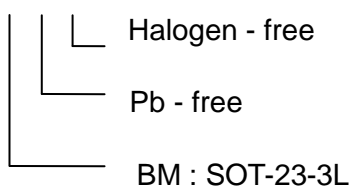


Marking



### Ordering information

ACE1710B XX + H





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### Electrical Characteristics

$T_A=25^{\circ}\text{C}$ , unless otherwise specified.

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Static						
Drain-source breakdown voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=250\mu A$	100	110		V
Zero gate voltage drain current	$I_{DSS}$	$V_{DS}=100V, V_{GS}=0V$			1	$\mu A$
Gate threshold voltage	$V_{GS(TH)}$	$V_{GS}=V_{DS}, I_{DS}=250\mu A$	1.2	1.8	2.5	V
Gate leakage current	$I_{GSS}$	$V_{GS}=\pm 20V, V_{DS}=0V$			$\pm 100$	nA
Drain-source on-state resistance	$R_{DS(ON)}$	$V_{GS}=10V, I_D=1A$		250	280	m $\Omega$
Forward transconductance	$g_{FS}$	$V_{DS}=5V, I_D=1A$	1.2			S
Diode forward voltage	$V_{SD}$	$I_{SD}=1.3A, V_{GS}=0V$			1.2	V
Maximum body-diode continuous current	$I_S$				1.6	A
Switching						
Total gate charge	Qg	$V_{GS}=10V, V_{DS}=50V, I_D=1.3A$		5.2		nC
Gate-source charge	Qgs			0.75		nC
Gate-drain charge	Qgd			1.4		nC
Turn-on delay time	$t_{d(on)}$	$V_{GS}=10V, R_L=39\Omega, I_D=1.3A, V_{DS}=50V, R_G=1\Omega$		6		ns
Turn-on rise time	tr			10		ns
Turn-off delay time	$t_{d(off)}$			10		ns
Turn-off fall time	tf			6		ns
Dynamic						
Input capacitance	Ciss	$V_{GS}=0V, V_{DS}=50V, F=1.0MHz$		190		pF
Output capacitance	Coss			22		pF
Reverse transfer capacitance	Crss			13		pF

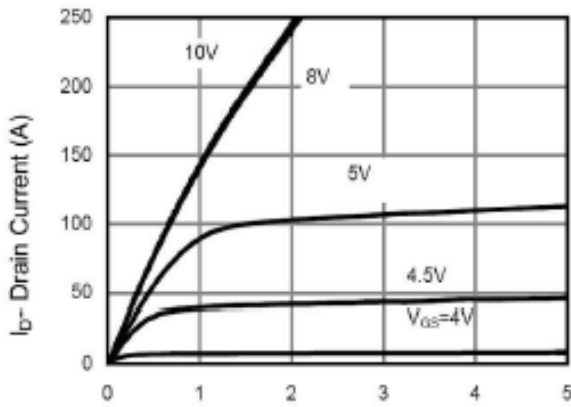
#### Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board,  $t \leq 10\text{sec}$ .
3. Pulse Test: Pulse Width  $\leq 300 \mu s$ , Duty Cycle  $\leq 2\%$ .
4. Guaranteed by design, not subject to production

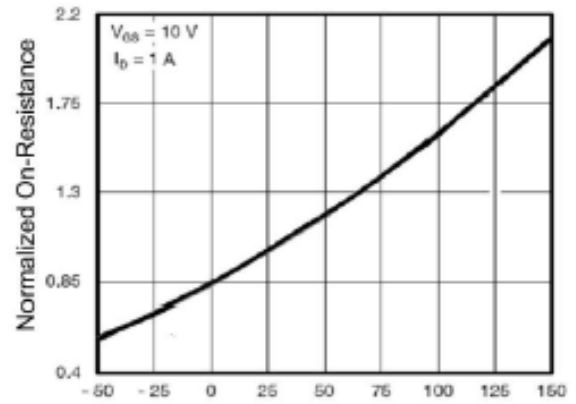


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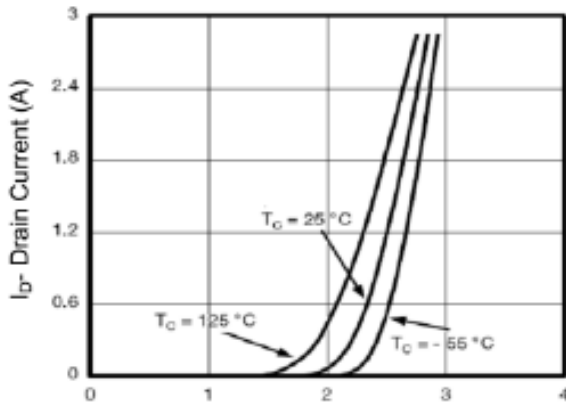
## Typical Performance Characteristics



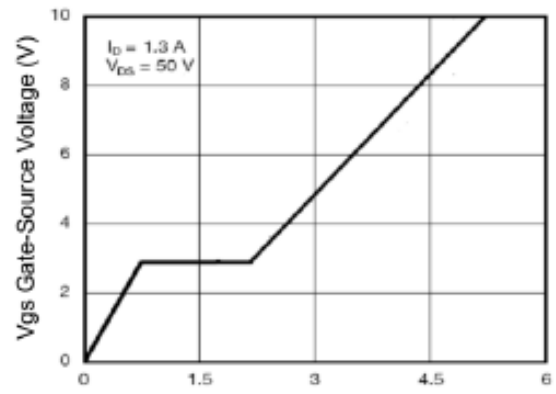
Vds Drain-Source Voltage(V)  
Figure 1 Output Characteristics



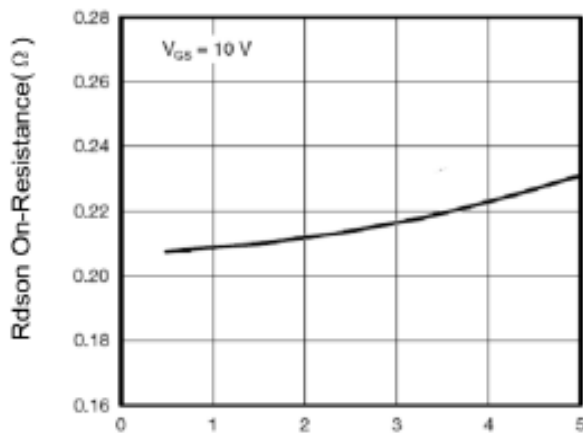
$T_J$  - Junction Temperature ( $^{\circ}C$ )  
Figure 4  $R_{ds(on)}$ -Junction Temperature



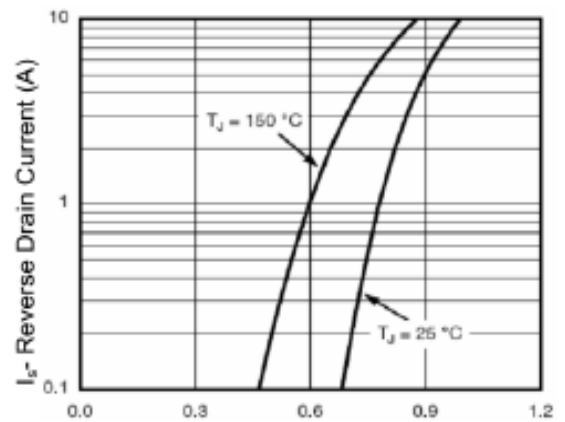
Vgs Gate-Source Voltage(V)  
Figure 2 Transfer Characteristics



$Q_G$  Gate Charge(nC)  
Figure 5 Gate Charge



$I_D$ -Drain Current(A)  
Figure 3  $R_{ds(on)}$  – Drain Current

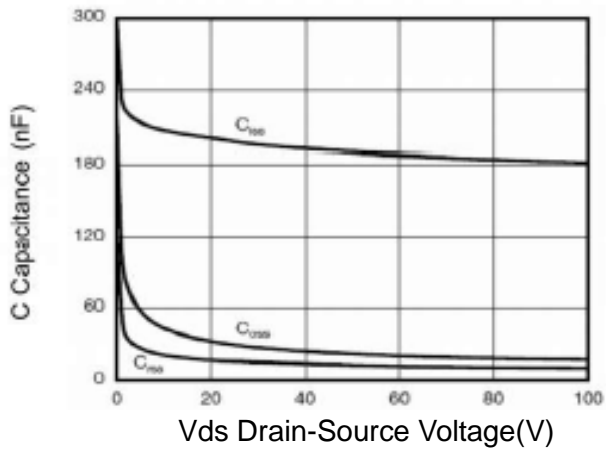


$V_{SD}$  Source-Drain Voltage(V)  
Figure 6 Source – Drain Diode Forward

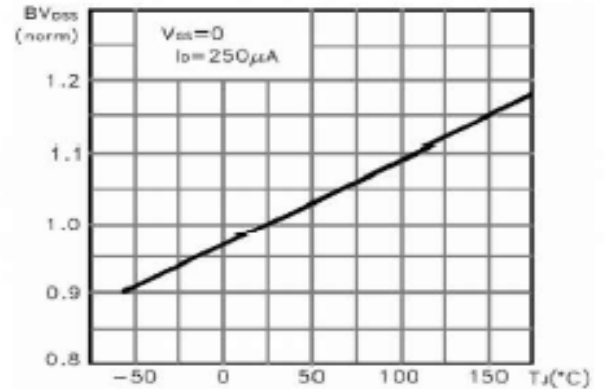


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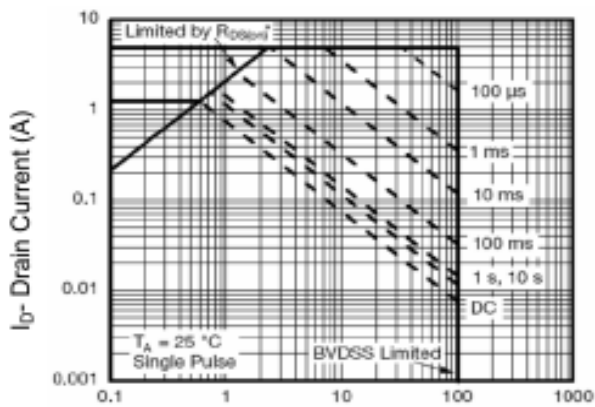
## Typical Performance Characteristics



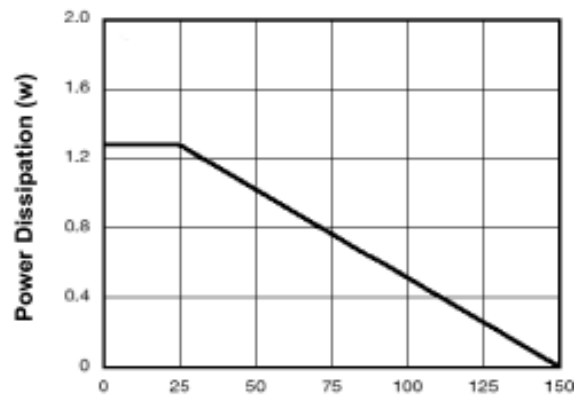
Vds Drain-Source Voltage(V)  
Figure 7 Capacitance vs Vds



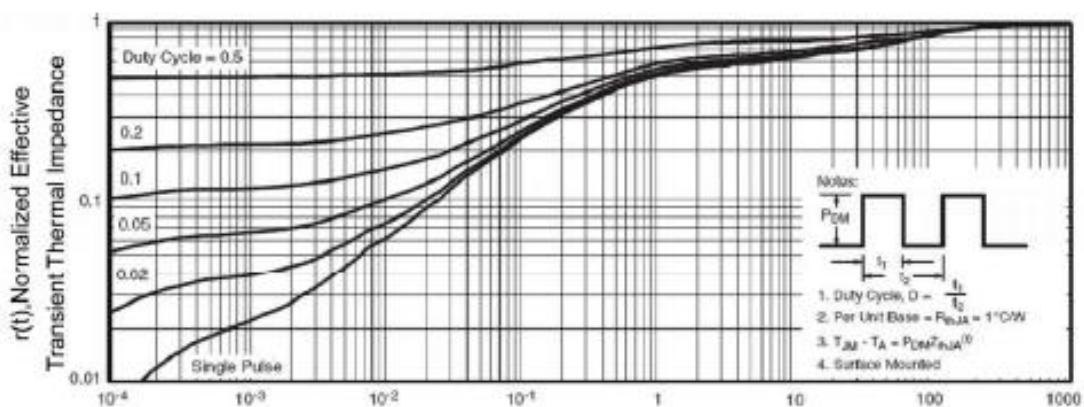
T<sub>J</sub> -Junction Temperature (°C)  
Figure 9 BV<sub>dss</sub> vs Junction Temperature



Vds Drain-Source Voltage(V)  
Figure 8 Safe Operation Area



T<sub>J</sub> -Junction Temperature (°C)  
Figure 10 Power De-rating



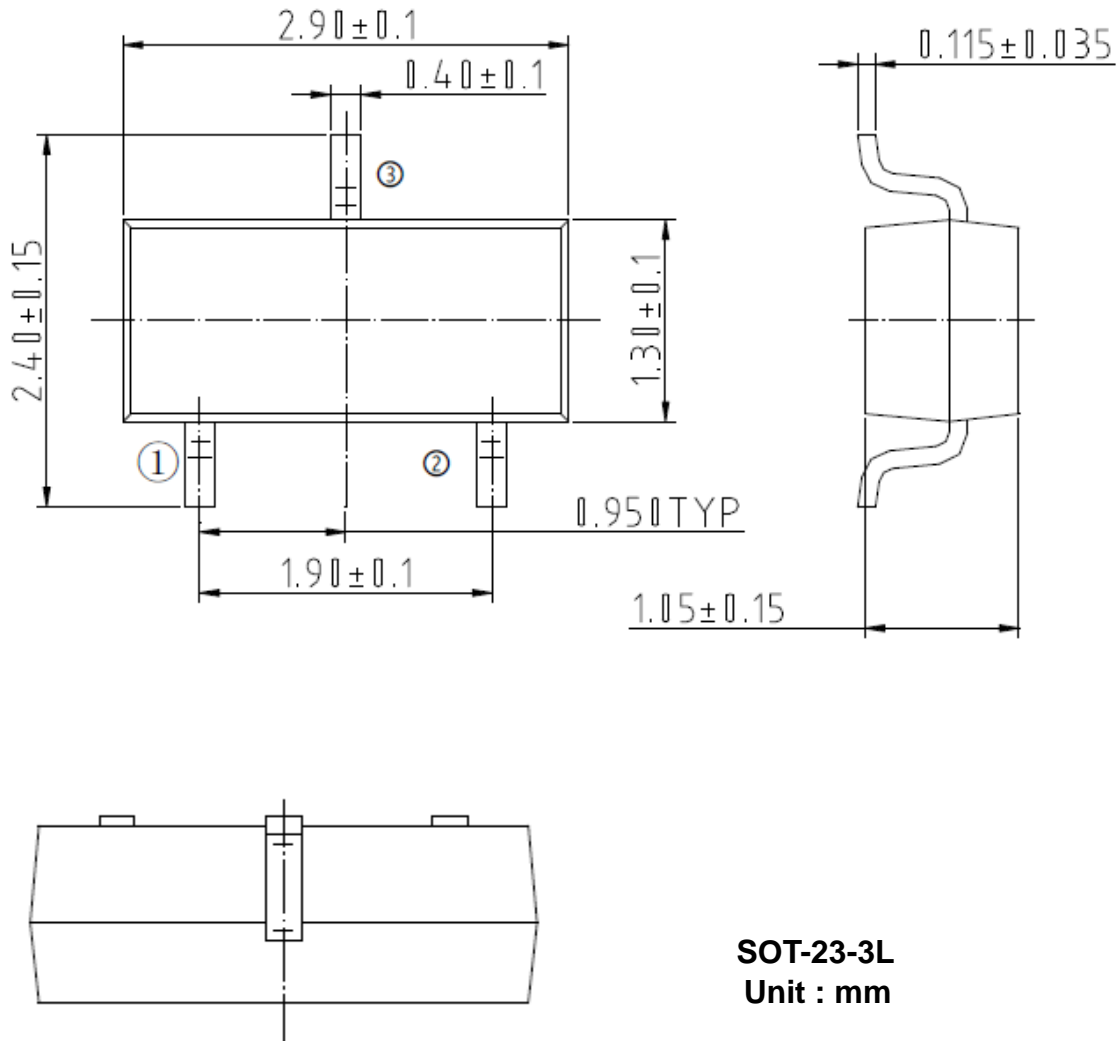
Square Wave Pulse Duration(sec)  
Figure 11 Normalized Maximum Transient Thermal Impedance



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## Packing Information

SOT-23-3L



SOT-23-3L  
Unit : mm



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### Notes

ACE does not assume any responsibility for use as critical components in life support devices or systems without the express written approval of the president and general counsel of ACE Electronics Co., LTD. As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury to the user.
2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

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