



ACE6602B

N-Channel Super Trench Power MOSFET

Description

ACE6602B uses Super Trench technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of $R_{DS(on)}$ and Q_g . This device is ideal for high-frequency switching and synchronous rectification.

Features

- $V_{DS} = 60V$, $I_D = 80A$
- $R_{DS(ON)} @ V_{GS} = 10V$, TYP= $3.5m\Omega$
- $R_{DS(ON)} @ V_{GS} = 4.5V$, TYP= $4m\Omega$

Absolute Maximum Ratings @ $T_A = 25^\circ C$ unless otherwise noted

| Parameter | Symbol | Max | Unit |
|---|--------------------|---------------------|------------|
| Drain-Source Voltage | V_{DSS} | 60 | V |
| Gate-Source Voltage | V_{GSS} | ± 20 | V |
| Drain Current (Continuous)*AC | I_D | $T_A = 25^\circ C$ | 80 |
| | | $T_A = 100^\circ C$ | 58 |
| Drain Current (Pulsed)*B | I_{DM} | 320 | A |
| Power Dissipation | $T_A = 25^\circ C$ | P_D | 85 |
| Operating temperature / Storage temperature | T_J / T_{STG} | -55~150 | $^\circ C$ |

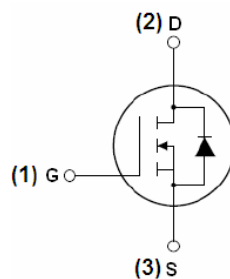
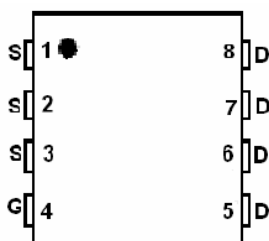
A: The value of $R_{\theta JA}$ is measured with the device mounted on $1in^2$ FR-4 board with 2oz. Copper, in a still air environment with $T_A = 25^\circ C$. The value in any given application depends on the user's specific board design.

B: Repetitive rating, pulse width limited by junction temperature.

C: The current rating is based on the $t \leq 10s$ junction to ambient thermal resistance rating.

Packaging Type

DFN5*6-EP





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Ordering information

ACE6602B XX + H

| | |
|--------|----------------|
| └──┬── | Halogen - free |
| └──┬── | Pb - free |
| └──┬── | PN: DFN5*6-EP |

Electrical Characteristics T_A=25°C, unless otherwise specified.

| Parameter | Symbol | Test Conditions | Min | Typ | Max | Unit |
|---------------------------------------|---------------|--|-----|------|-----------|------------|
| Static | | | | | | |
| Drain-source breakdown voltage | $V_{(BR)DSS}$ | $V_{GS}=0V, I_D=250\mu A$ | 60 | | | V |
| Zero gate voltage drain current | I_{DSS} | $V_{DS}=60V, V_{GS}=0V$ | | | 1 | μA |
| Gate threshold voltage | $V_{GS(th)}$ | $V_{GS}=V_{DS}, I_{DS}=250\mu A$ | 1 | 1.7 | 2.4 | V |
| Gate leakage current | I_{GSS} | $V_{GS}=\pm 20V, V_{DS}=0V$ | | | ± 100 | μA |
| Drain-source on-state resistance | $R_{DS(on)}$ | $V_{GS}=10V, I_D=40A$ | | 3.5 | 4 | m Ω |
| | | $V_{GS}=4.5V, I_D=40A$ | | 4 | 5 | |
| Forward Trans conductance | g_{FS} | $V_{DS}=10V, I_D=40A$ | 40 | | | S |
| Diode forward voltage | V_{SD} | $I_{SD}=80A, V_{GS}=0V$ | | | 1.2 | V |
| Maximum body-diode continuous current | I_S | | | | 80 | A |
| Switching | | | | | | |
| Total Gate Charge | Q_g | $V_{GS}=10V, V_{DS}=30V, I_D=40A$ | | 67 | | nC |
| Gate-Source Charge | Q_{gs} | | | 12 | | |
| Gate-Drain Charge | Q_{gd} | | | 8.5 | | |
| Turn-on Delay Time | $t_{d(on)}$ | $V_{DD}=30V, I_D=40A, V_{GS}=10V, R_G=4.7\Omega$ | | 11 | | ns |
| Turn-on Rise Time | t_r | | | 5 | | |
| Turn-off Delay Time | $t_{d(off)}$ | | | 56 | | |
| Turn-off Fall Time | t_f | | | 12 | | |
| Dynamic | | | | | | |
| Input Capacitance | C_{iss} | $V_{GS}=0V, V_{DS}=30V, f=1.0MHz$ | | 4000 | | pF |
| Output Capacitance | C_{oss} | | | 680 | | |
| Reverse Transfer Capacitance | C_{rss} | | | 23 | | |



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Typical Performance Characteristics

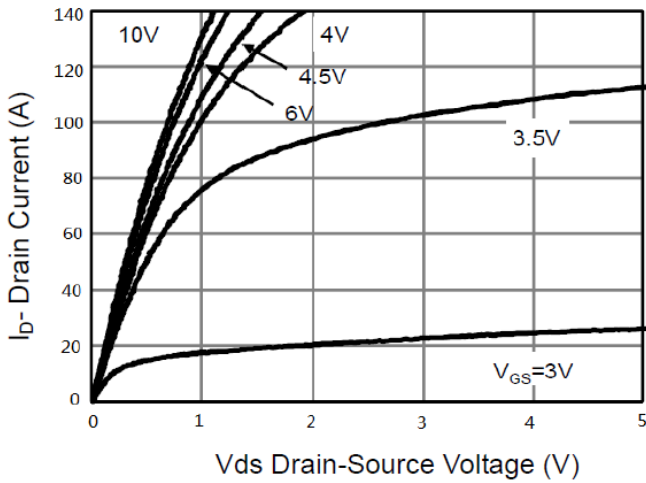


Figure 1 Output Characteristics

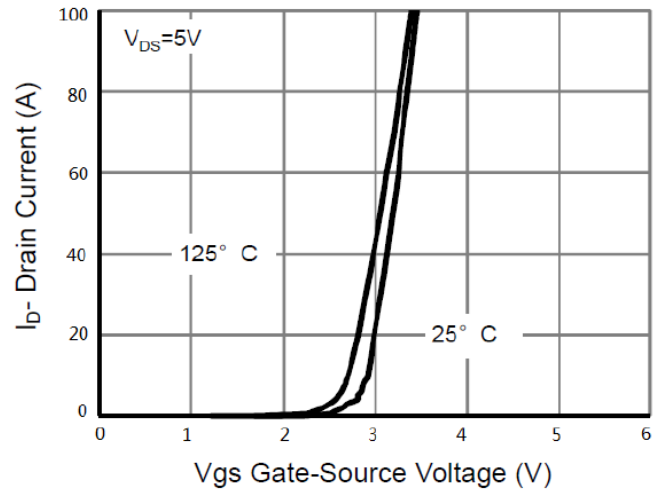


Figure 2 Transfer Characteristics

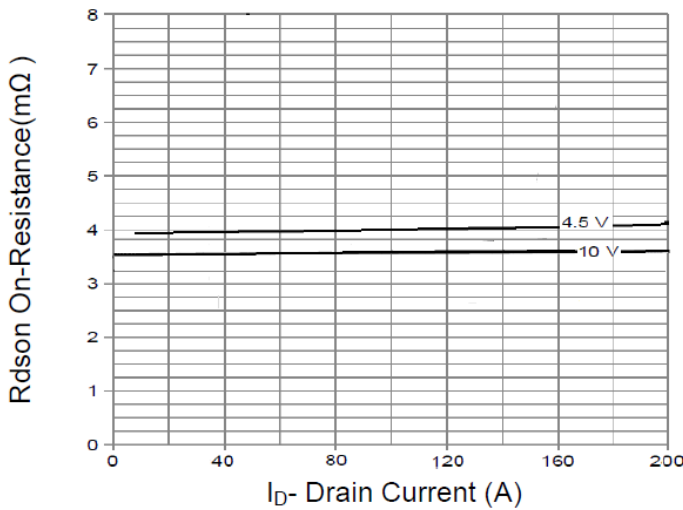


Figure 3 Rdson- Drain Current

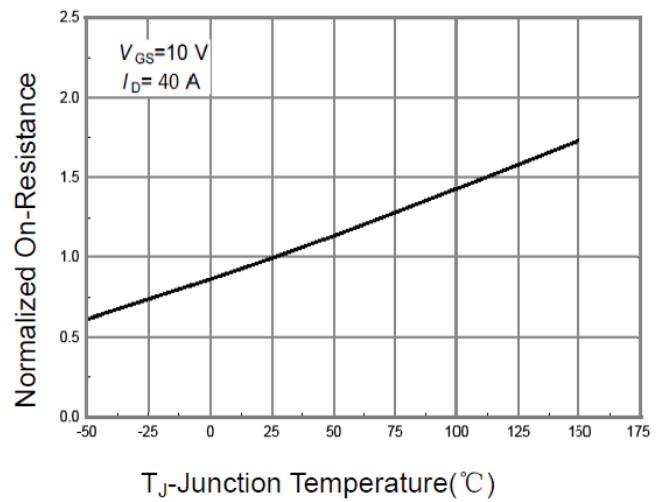


Figure 4 Rdson-Junction Temperature

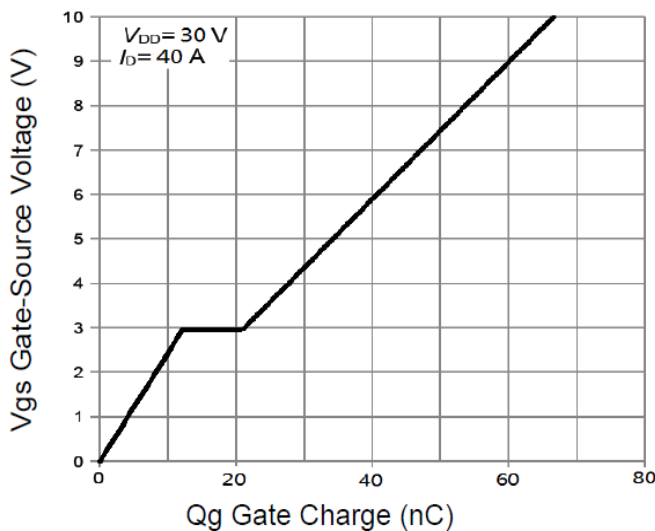


Figure 5 Gate Charge

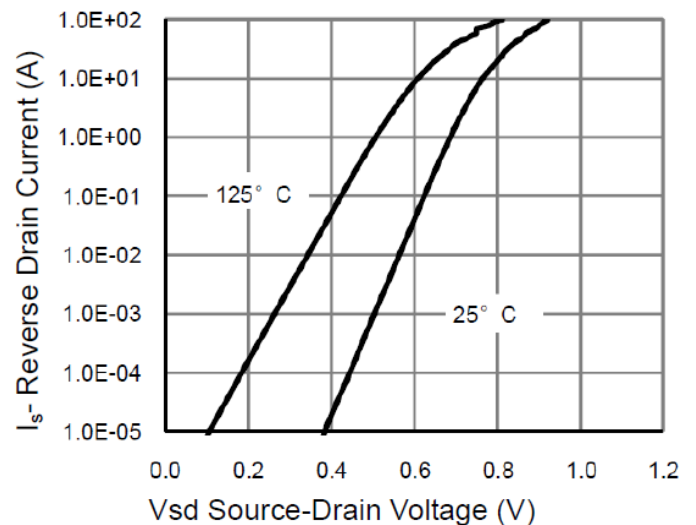


Figure 6 Source- Drain Diode Forward



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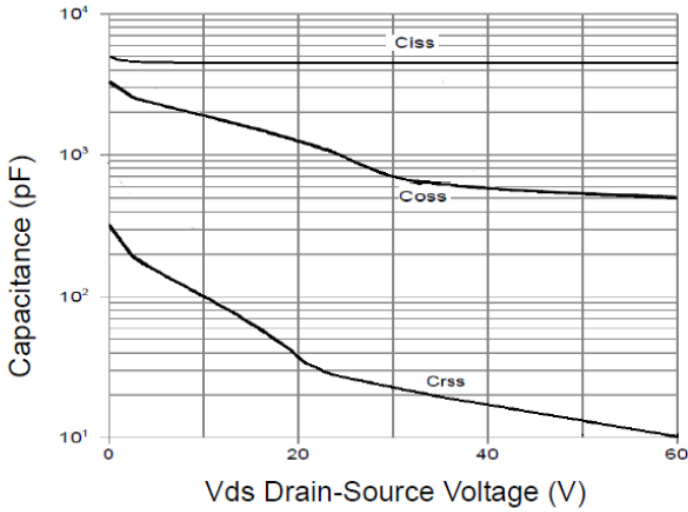


Figure 7 Capacitance vs Vds

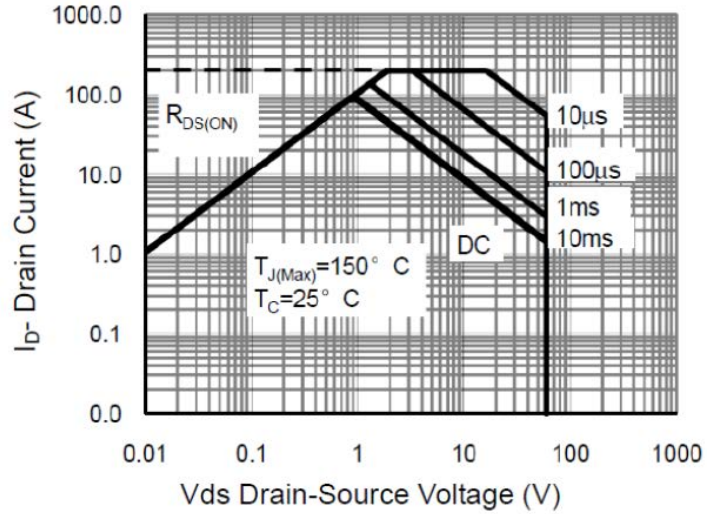


Figure 8 Safe Operation Area

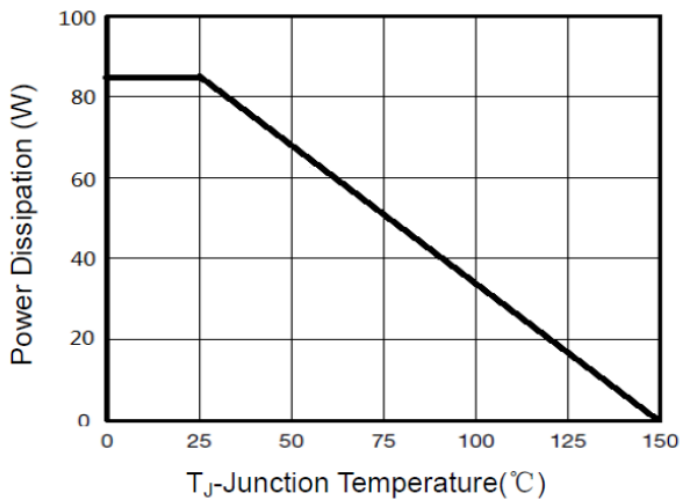


Figure 9 Power De-rating

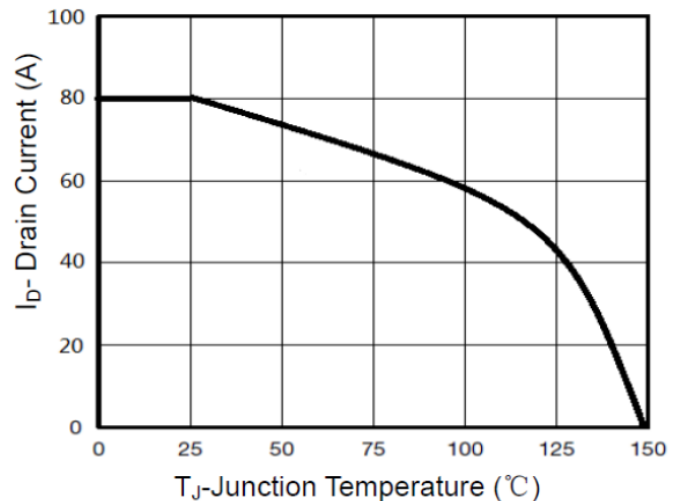


Figure 10 Current De-rating

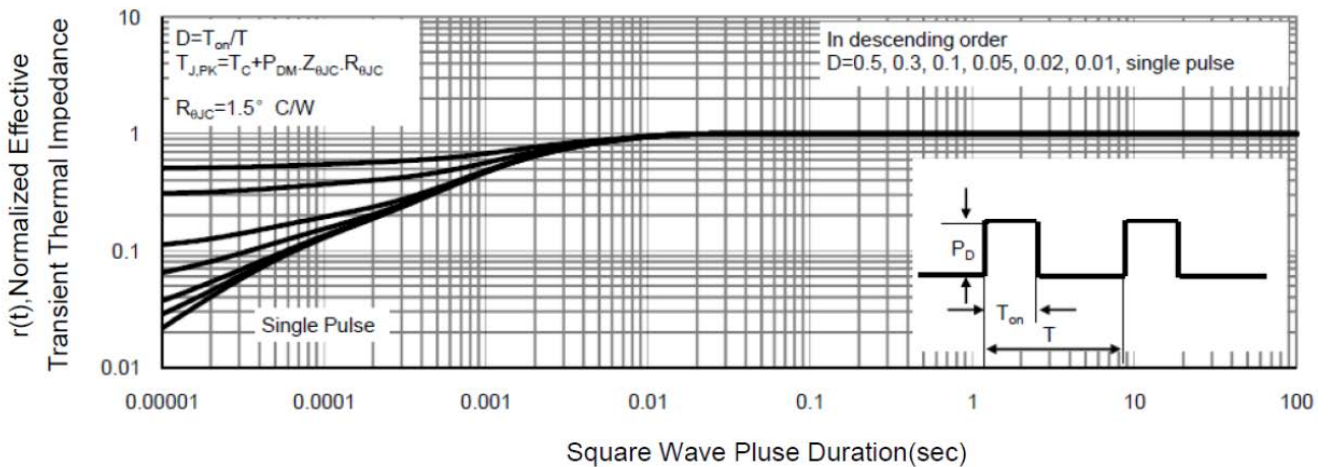


Figure 11 Normalized Maximum Transient Thermal Impedance

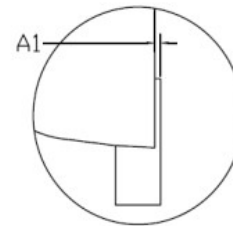
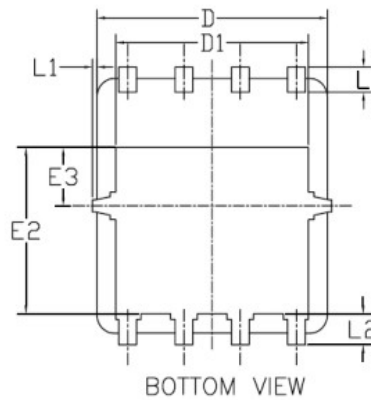
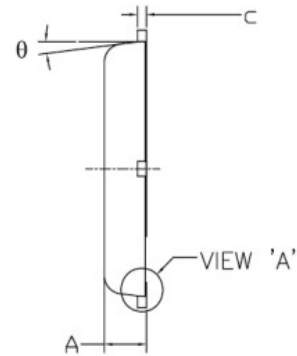
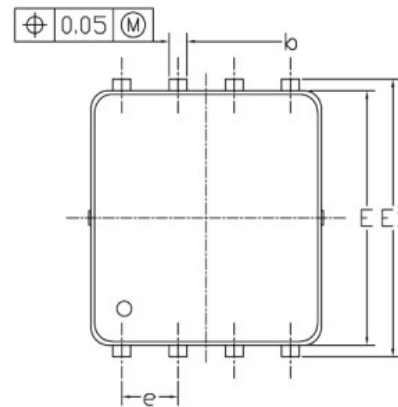


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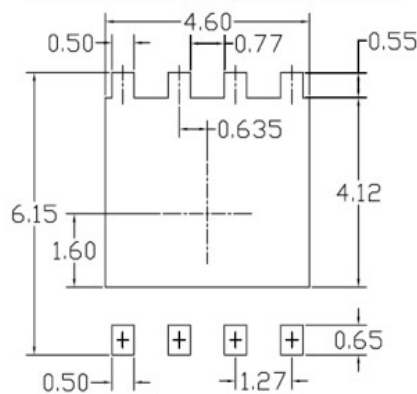
Packing Information

DFN5*6-EP



VIEW 'A'
(SCALE 5:1)

RECOMMENDED LAND PATTERN



| SYMBOLS | DIMENSIONS IN MILLIMETERS | | | DIMENSIONS IN INCHES | | |
|---------|---------------------------|------|------|----------------------|-------|-------|
| | MIN | NOM | MAX | MIN | NOM | MAX |
| A | 0.85 | 0.95 | 1.00 | 0.033 | 0.037 | 0.039 |
| A1 | 0.00 | — | 0.05 | 0.000 | — | 0.002 |
| b | 0.30 | 0.40 | 0.50 | 0.012 | 0.016 | 0.020 |
| c | 0.15 | 0.20 | 0.25 | 0.006 | 0.008 | 0.010 |
| D | 5.20 BSC | | | 0.205 BSC | | |
| D1 | 4.35 BSC | | | 0.171 BSC | | |
| E | 5.55 BSC | | | 0.219 BSC | | |
| E1 | 6.05 BSC | | | 0.238 BSC | | |
| E2 | 3.625 BSC | | | 0.143 BSC | | |
| E3 | 1.275 BSC | | | 0.050 BSC | | |
| e | 1.27 BSC | | | 0.050 BSC | | |
| L | 0.45 | 0.55 | 0.65 | 0.018 | 0.022 | 0.026 |
| L1 | 0 | — | 0.15 | 0 | — | 0.006 |
| L2 | 0.68 REF | | | 0.027 REF | | |
| θ | 0° | — | 10° | 0° | — | 10° |

NOTE

UNIT: mm

- PACKAGE BODY SIZES EXCLUDE MOLD FLASH AND GATE BURRS. MOLD FLASH AT THE NON-LEAD SIDES SHOULD BE LESS THAN 6 MILS EACH.
- CONTROLLING DIMENSION IS MILLIMETER. CONVERTED INCH DIMENSIONS ARE NOT NECESSARILY EXACT.



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Notes

ACE does not assume any responsibility for use as critical components in life support devices or systems without the express written approval of the president and general counsel of ACE Electronics Co., LTD.

As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury to the user.
2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

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